

Tzchen (Edison) Wu

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Education

University of Nevada – Las Vegas

Fall 2027

Bachelor of Science in Electrical Engineering, Minor in Japanese

Technical Skills

UC OASiS Foundations – Semiconductor Processing & IC Fabrication

Apr 2026

Programming: Swift, SystemVerilog, C/C++

Software & Tools: Git/GitHub, Docker, Linux/Ubuntu, Quartus II, SolidWorks, Ollama

Hardware: FPGA, circuit prototyping, breadboarding, soldering, 3D printing

Languages: English (Native), Mandarin (Native), Japanese (Conversational)

Work Experience

UGym Technology Corp. – Sales & Marketing Intern, CES

Jan 2023 & Jan 2024

- Represented UGym Technology at CES, communicating product capabilities to prospective customers and industry visitors while providing real-time Mandarin–English interpretation
- Supported product demonstrations and customer engagement throughout the exhibition

Surglasses – Engineering Intern

Feb 2022 – Mar 2022

- Supported assembly and manufacturing of electronic products, including component soldering
- Translated technical documentation between Mandarin and English

Point Robotics Medtech Inc. – Engineering Intern

Jun 2021

- Created 3D models and designs to support engineering development using SolidWorks
- Translated technical documentation between Mandarin and English

Projects

Eddie Calendar – iOS Calendar Application

Spring 2026 – Present

- Developed a native iOS calendar integrating Google Calendar, Apple Calendar, and local events
- Built custom month/week/day interfaces with categories, recurring events, filtering, and color management
- Implemented calendar synchronization, notifications, and weather integration

Personal AI Location Server – Ubuntu, Docker, Dawarich, Tailscale, Ollama

Spring 2026

- Deployed a self-hosted server to securely ingest, store, and visualize continuous iPhone GPS data
- Integrated Ollama for local natural-language querying, summarization, and analysis of location history
- Troubleshoot networking, containers, database ingestion, GPU, and mobile-to-server integration

USB Power Supply & Charging Circuit

Spring 2026

- Designed and prototyped a regulated 5 V USB supply using an LM7805 regulator and discrete components
- Implemented USB charging signaling and tested voltage regulation, load behavior, power dissipation, and thermal limitations

Car Turn Signal Controller – SystemVerilog, Quartus II, FPGA

Fall 2024

- Designed and simulated an FSM-based turn-signal controller in SystemVerilog
- Implemented and tested the design on a DE2-115 FPGA using Quartus II

Leadership & Activities

- Founder & President, UNLV Run Club Aug 2026 – Present
- UNLV Marching Band – Trombone Aug 2022 – Present
- Trombone Section Leader Aug 2024 – May 2026
- UNLV Pep Band Oct 2022 – Present
- Study Abroad – Hiroshima University Apr – Aug 2022